

FS6S0765RCH

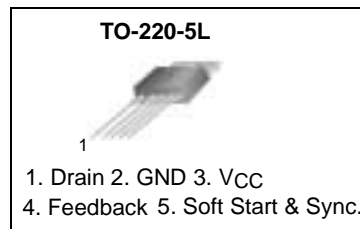
Fairchild Power Switch(FPS)

Features

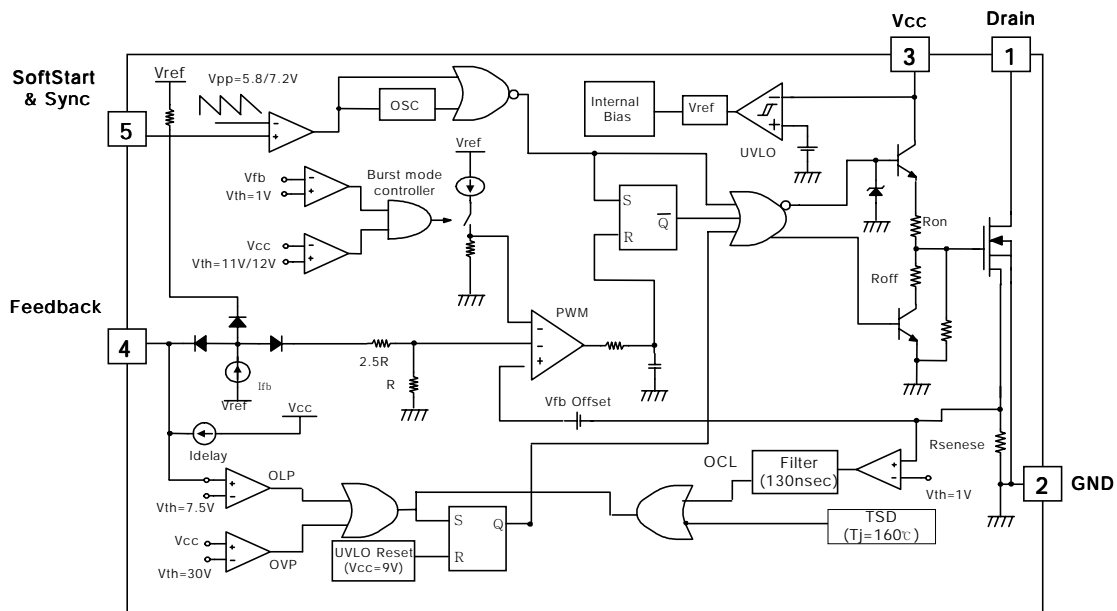
- Wide Operating Frequency Range up to 150KHz
- Internal Burst Mode Controller for Stand-by Mode
- Pulse by Pulse Over Current Limiting
- Over Current Protection(Auto Restart Mode)
- Over Voltage Protection (Auto Restart Mode)
- Over Load Protection(Auto Restart Mode)
- Internal Thermal Shutdown Function(Auto Restart Mode)
- Under Voltage Lockout
- Internal High Voltage SenseFET(CFET)
- Eternal Sync Terminal/Soft Start

Description

The Fairchild Power Switch(FPS) product family is specially designed for an off line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM IC. Included PWM controller features integrated fixed oscillator, under voltage lock out, optimized gate turn on/turn off driver, thermal shutdown protection, over voltage protection, and temperature compensated precision current sources for loop compensation and fault protection circuitry. Compared with discrete MOSFET and controller or RCC switching converter solution, the Fairchild Power Switch(FPS) can reduce total component count, design size, and weight and at the same time increase efficiency, productivity and system reliability. It has a basic platform well suited for cost effective monitor power supply.



Internal Block Diagram



Absolute Maximum Ratings

(Ta=25°C, unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-Source(GND) Voltage ⁽¹⁾	V _{DSS}	650	V
Drain-Gate Voltage (R _{GS} =1MΩ)	V _{DGR}	650	V
Gate-Source (GND) Voltage	V _{GS}	±30	V
Drain Current Pulsed ⁽²⁾	I _{DM}	28	ADC
Single Pulsed Avalanche Energy ⁽³⁾	E _{AS}	370	mJ
Single Pulsed Avalanche Current ⁽⁴⁾	I _{AS}	17	A
Continuous Drain Current (T _c = 25°C)	I _D	7	ADC
Continuous Drain Current (T _C =100°C)	I _D	4.5	ADC
Supply Voltage	V _{CC}	35	V
Input Voltage Range	V _{FB}	-0.3 to V _{CC}	V
	V _{S_S}	-0.3 to 10	V
Total Power Dissipation	P _D (Watt H/S)	145	W
	Darting	1.16	W/°C
Operating Junction Temperature	T _j	+150	°C
Operating Ambient Temperature	T _A	-25 to +85	°C
Storage Temperature Range	T _{STG}	-55 to +150	°C

Note:

1. T_j = 25°C to 150°C
2. Repetitive rating: Pulse width limited by maximum junction temperature
3. L = 14mH, starting T_j = 25°C
4. L = 13uH, starting T_j = 25°C

Electrical Characteristics (SenseFET part)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BVDSS	VGS = 0V, ID = 250μA	650	-	-	V
Zero Gate Voltage Drain Current	IDSS	VDS = 650V, VGS = 0V	-	-	200	μA
		VDS = 520V VGS = 0V, TC = 125°C	-	-	300	μA
Static Drain-Source on Resistance ^(note)	RDS(ON)	VGS = 10V, ID = 3.5A	-	1.4	1.6	Ω
Forward Transconductance	gfs	VDS = 40V, ID = 3.5A	-	8	-	mho
Input Capacitance	Ciss	VGS = 0V, VDS = 25V, f = 1MHz	-	1415	-	pF
Output Capacitance	Coss		-	100	-	
Reverse Transfer Capacitance	Crss		-	15	-	
Turn on Delay Time	td(on)	VDD = 325V, ID = 6.5A (MOSFET switching time is essentially independent of operating temperature)	-	25	-	nS
Rise Time	tr		-	60	-	
Turn Off Delay Time	td(off)		-	115	-	
Fall Time	tf		-	65	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	VGS = 10V, ID = 6.5A, VDS = 520V (MOSFET switching time is essentially independent of operating temperature)	-	40	-	nC
Gate Source Charge	Qgs		-	7	-	
Gate Drain (Miller) Charge	Qgd		-	12	-	

Note:

Pulse test : Pulse width ≤ 300μS, duty 2%

Electrical Characteristics (Continued)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
UVLO SECTION						
Start Threshold Voltage	VSTART	VFB = GND	14	15	16	V
Stop Threshold Voltage	VSTOP	VFB = GND	8	9	10	V
OSCILLATOR SECTION						
Initial Frequency	FOSC	-	22	25	28	kHz
Voltage Stability	FSTABLE	12V ≤ Vcc ≤ 23V	0	1	3	%
Temperature Stability ⁽²⁾	ΔFOSC	-25°C ≤ Ta ≤ 85°C	0	±5	±10	%
Maximum Duty Cycle	DMAX	-	92	95	98	%
Minimum Duty Cycle	DMIN	-	-	-	0	%
FEEDBACK SECTION						
Feedback Source Current	IFB	VFB = GND	0.7	0.9	1.1	mA
Shutdown Feedback Voltage	VSD	Vfb ≥ 6.9V	6.9	7.5	8.1	V
Shutdown Delay Current	Idelay	VFB = 5V	1.6	2.0	2.4	μA
PROTECTION SECTION						
Over Voltage Protection	VOVP	VCC ≥ 27V	27	30	33	V
Over Current Latch Voltage ⁽¹⁾	VOCL	-	0.9	1.0	1.1	V
Thermal Shutdown Temp. ⁽²⁾	TSD	-	140	160	-	°C

Note:

1. These parameters, although guaranteed, are tested in EDS(wafer test) process.
2. These parameters, although guaranteed at the design, are not tested in mass production

Electrical Characteristics (Continued)

(Ta=25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
SYNC & SOFTSTART SECTION						
Softstart Voltage	VSS	Vfb = 2	4.7	5.0	5.3	V
Softstart Current	ISS	Vss = V	0.8	1.0	1.2	mA
Sync High Threshold Voltage	VSH	Vcc = 16V, Vfb = 5V	-	7.2	-	V
Sync Low Threshold Voltage	VSL	Vcc = 16V, Vfb = 5V	-	5.8	-	V
BURST MODE SECTION						
Burst Mode Low Threshold Voltage	VBURL	Vfb = 0V	10.4	11.0	11.6	V
Burst Mode High Threshold Voltage	VBURH	Vfb = 0V	11.4	12.0	12.6	V
Burst Mode Enable Feedback Voltage ⁽³⁾	VBEN	Vcc = 10.5V	0.7	1.0	1.3	V
Burst Mode Peak Current Limit ⁽²⁾	IBU_PK	Vcc = 10.5V	0.45	0.6	0.75	V
Burst Mode Frequency	FBUR	Vcc = 10.5V, Vfb = 0V	40	50	60	KHz
CURRENT LIMIT(SELF-PROTECTION)SECTION						
Peak Current Limit ⁽²⁾	IOVER	-	3.52	4.0	4.48	A
TOTAL DEVICE SECTION						
Start Up Current	ISTART	Vfb = GND, VCC = 14V	-	0.1	0.17	mA
Operating Supply Current ⁽¹⁾	IOP	Vfb = GND, VCC = 16V	-	10	15	mA
	IOP(MIN)	Vfb = GND, VCC = 10V				
	IOP(MAX)	Vfb = GND, VCC = 28V				

Note:

1. These parameters are the current flowing in the control IC.
2. These parameters indicate Inductor current.
3. These parameters, although guaranteed at the design, are not tested in mass production.

Typical Performance Characteristics

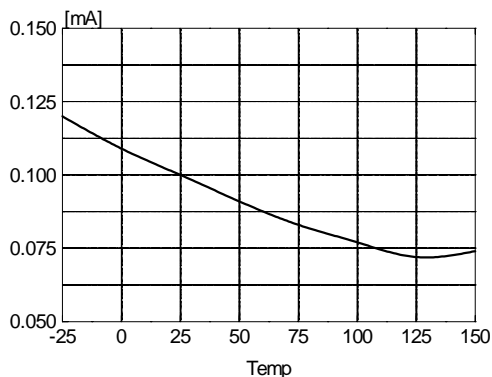


Figure 1. Start Up Current vs. Temp.

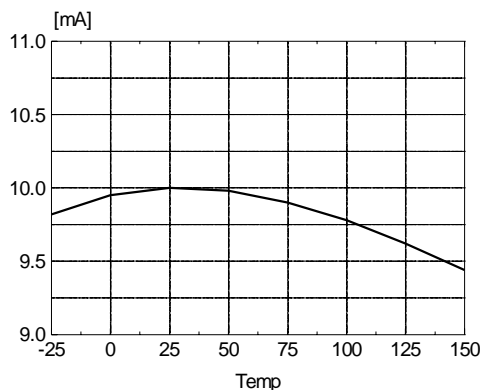


Figure 2. Operating Current vs. Temp.

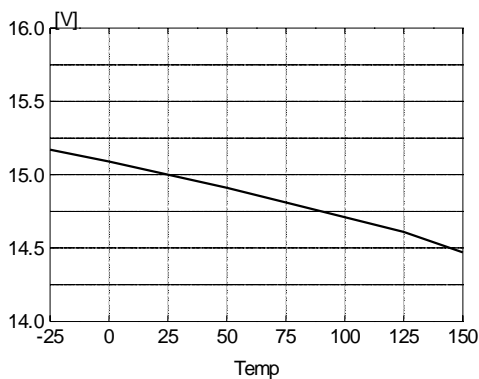


Figure 3. Start Threshold Voltage vs. Temp.

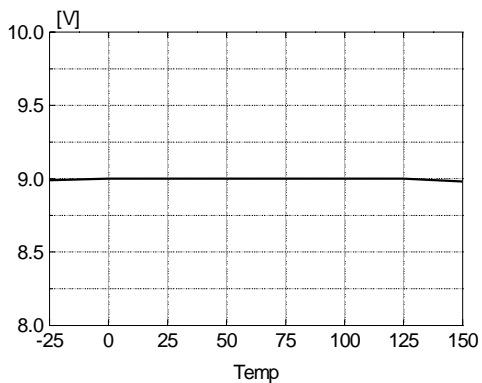


Figure 4. Stop Threshold Voltage vs. Temp.

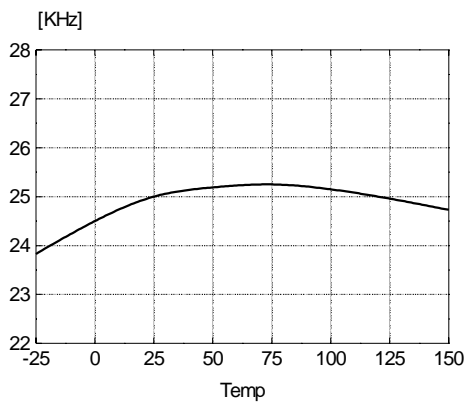


Figure 5. Initial Frequency vs. Temp.

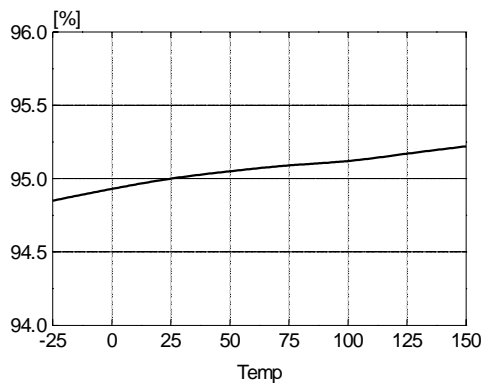


Figure 6. Maximum Duty vs. Temp.

Typical Performance Characteristics (Continued)

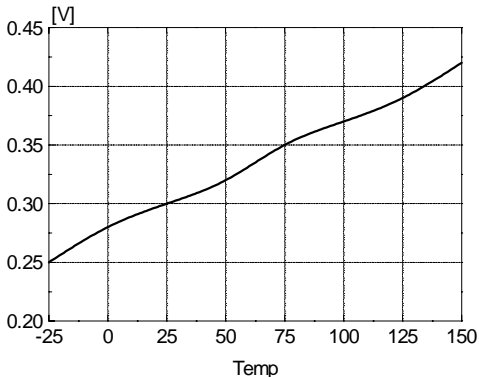


Figure 7. Feedback Offset Voltage vs. Temp.

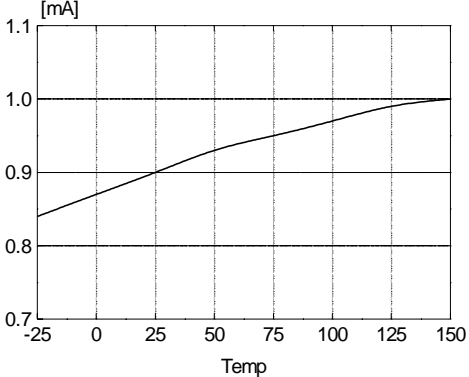


Figure 8. Feedback Source Current vs. Temp.

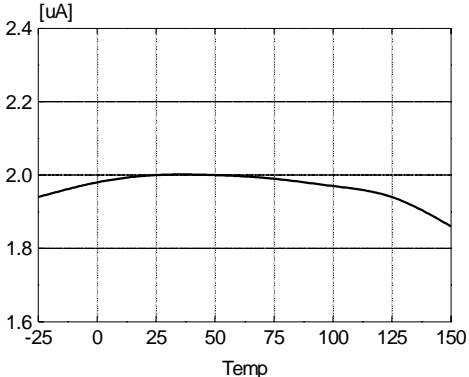


Figure 9. Shutdown Delay Current vs. Temp.

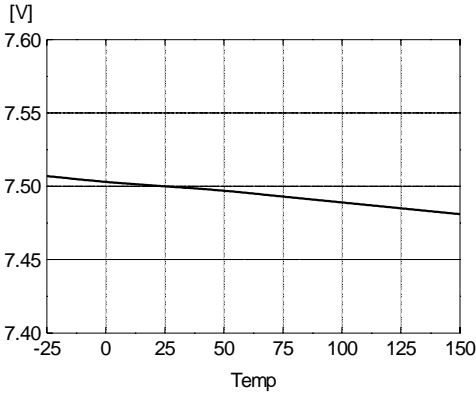


Figure 10. Shutdown Feedback Voltage vs. Temp.

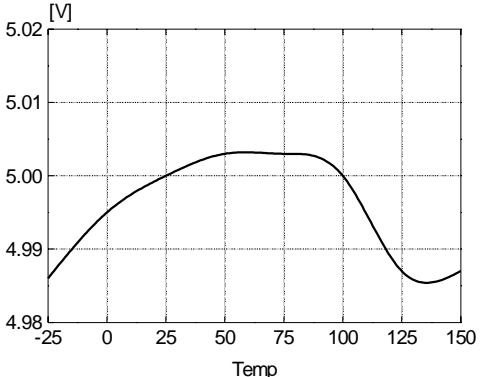


Figure 11. Softstart Voltage vs. Temp.

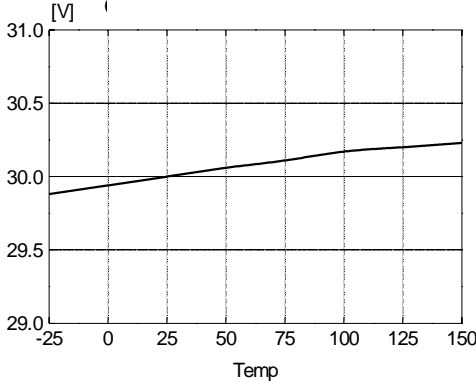


Figure 12. Over Voltage Protection vs. Temp.

Typical Performance Characteristics (Continued)

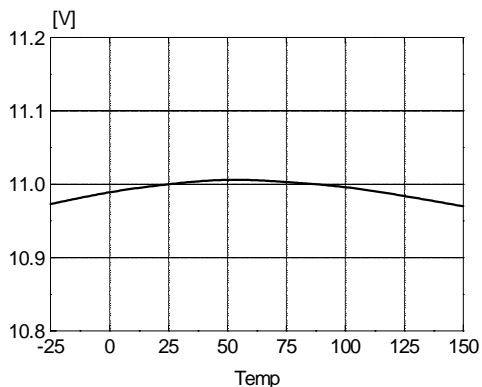


Figure 13. Burst Mode Low Voltage vs. Temp.

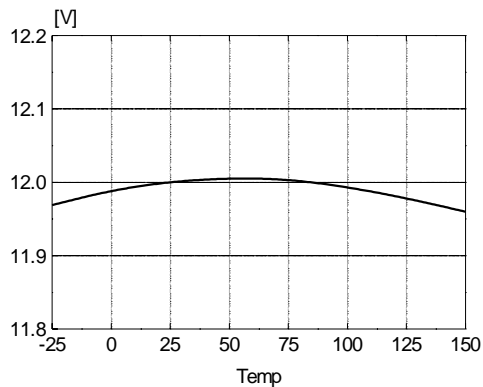


Figure 14. Burst Mode High Voltage vs. Temp.

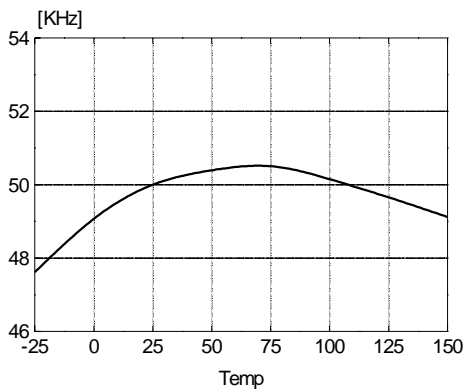


Figure 15. Burst Mode Frequency vs. Temp.

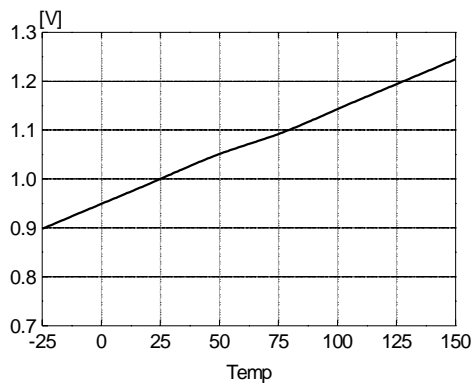


Figure 16. Burst Mode Enable Voltage vs. Temp.

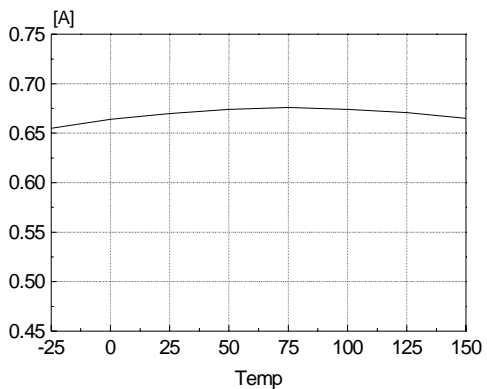


Figure 17. Burst Mode Peak Current vs. Temp.

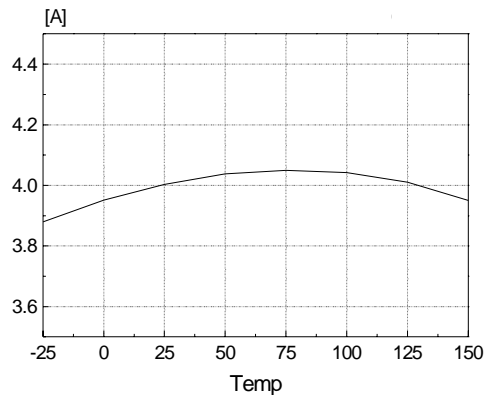
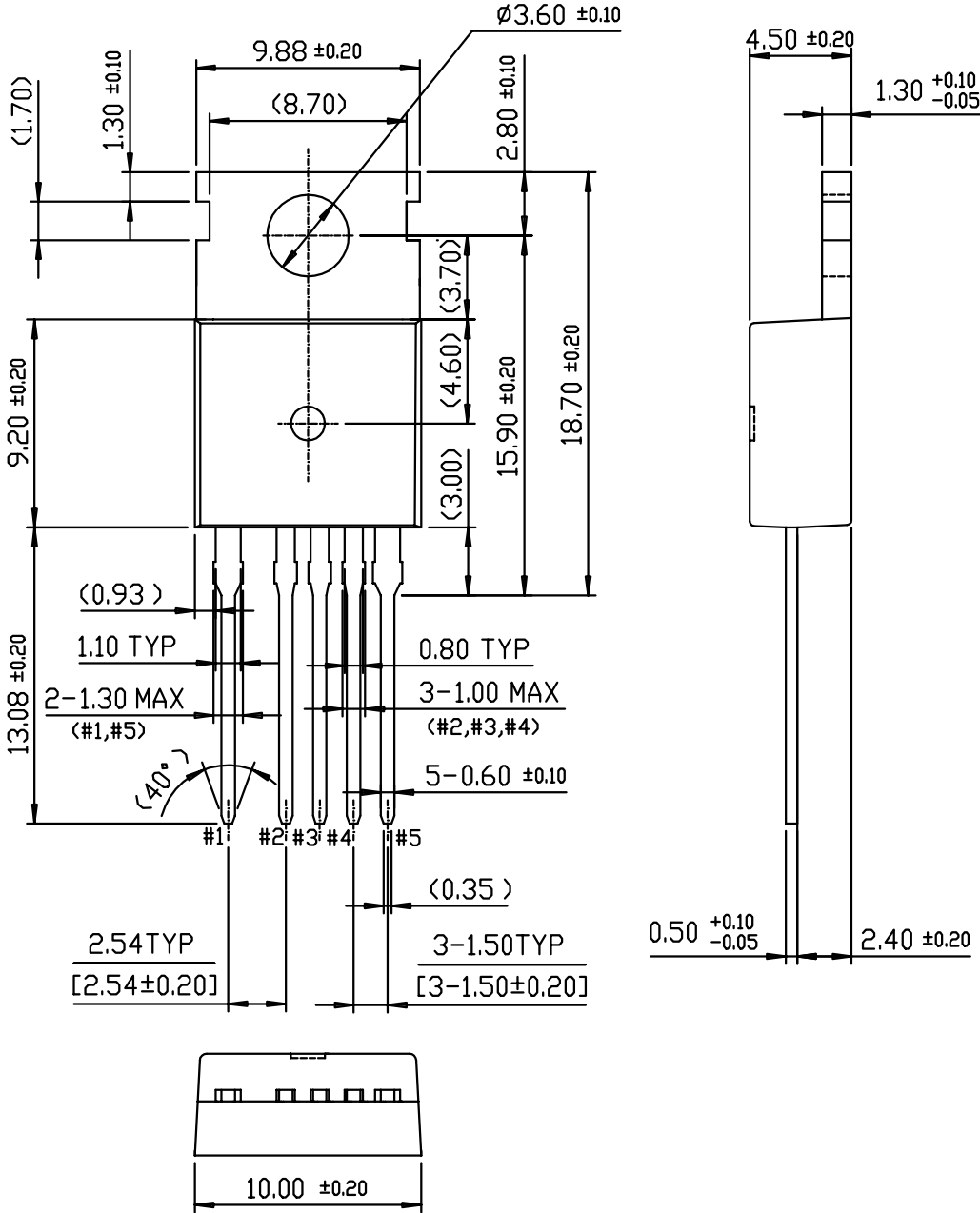


Figure 18. Peak Current Limit vs. Temp.

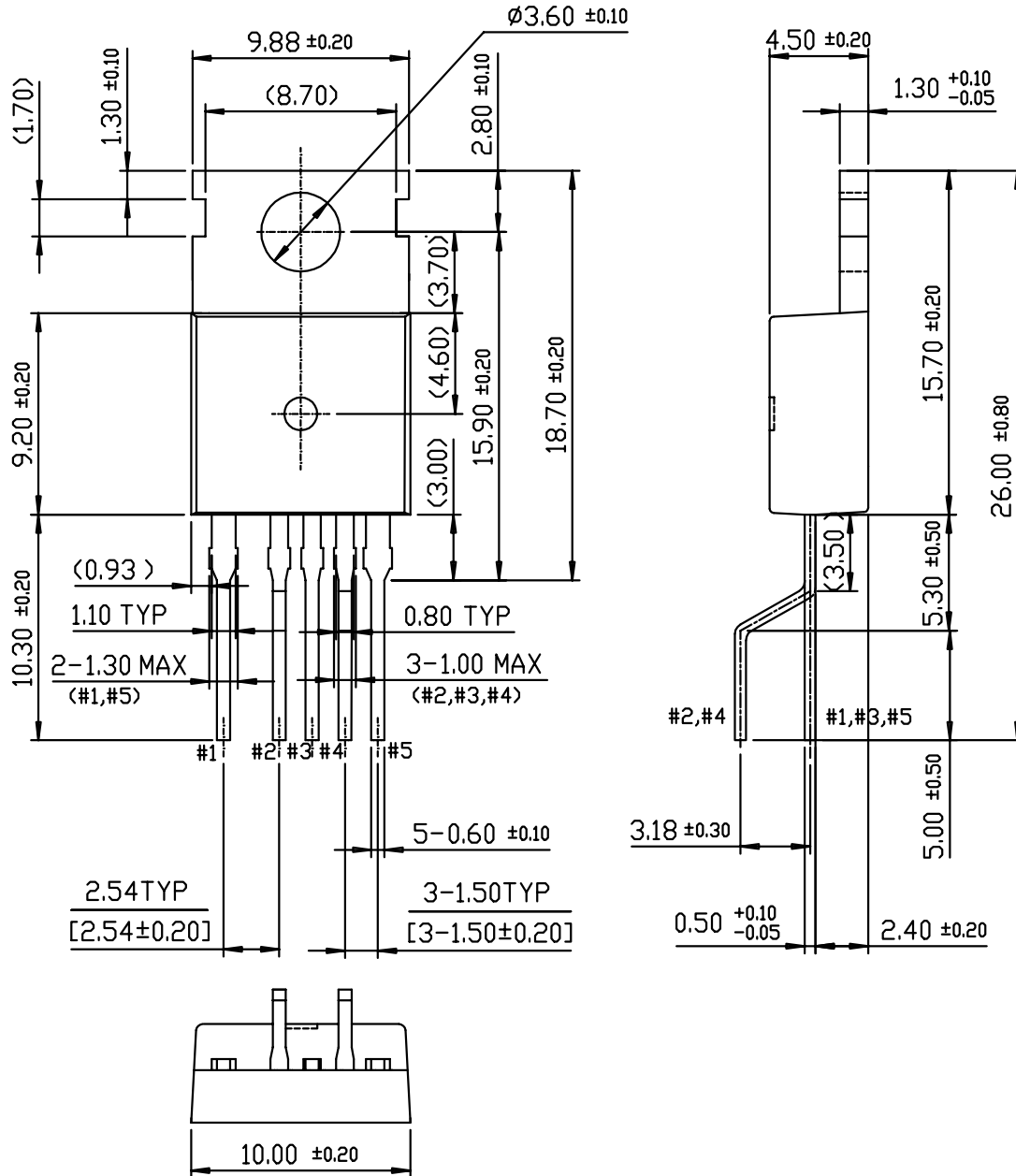
Package Dimensions

TO-220-5L



Package Dimensions (Continued)

TO-220-5L(Forming)



Ordering Information

Product Number	Package	Marking Code	BVdss	Rds(on)
FS6S0765RCHTU	TO-220-5L	6S0765RC H	650V	1.4
FS6S0765RCHYDTU	TO-220-5L(Forming)			

TU : Non Forming Type

YDTU : Forming Type

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2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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